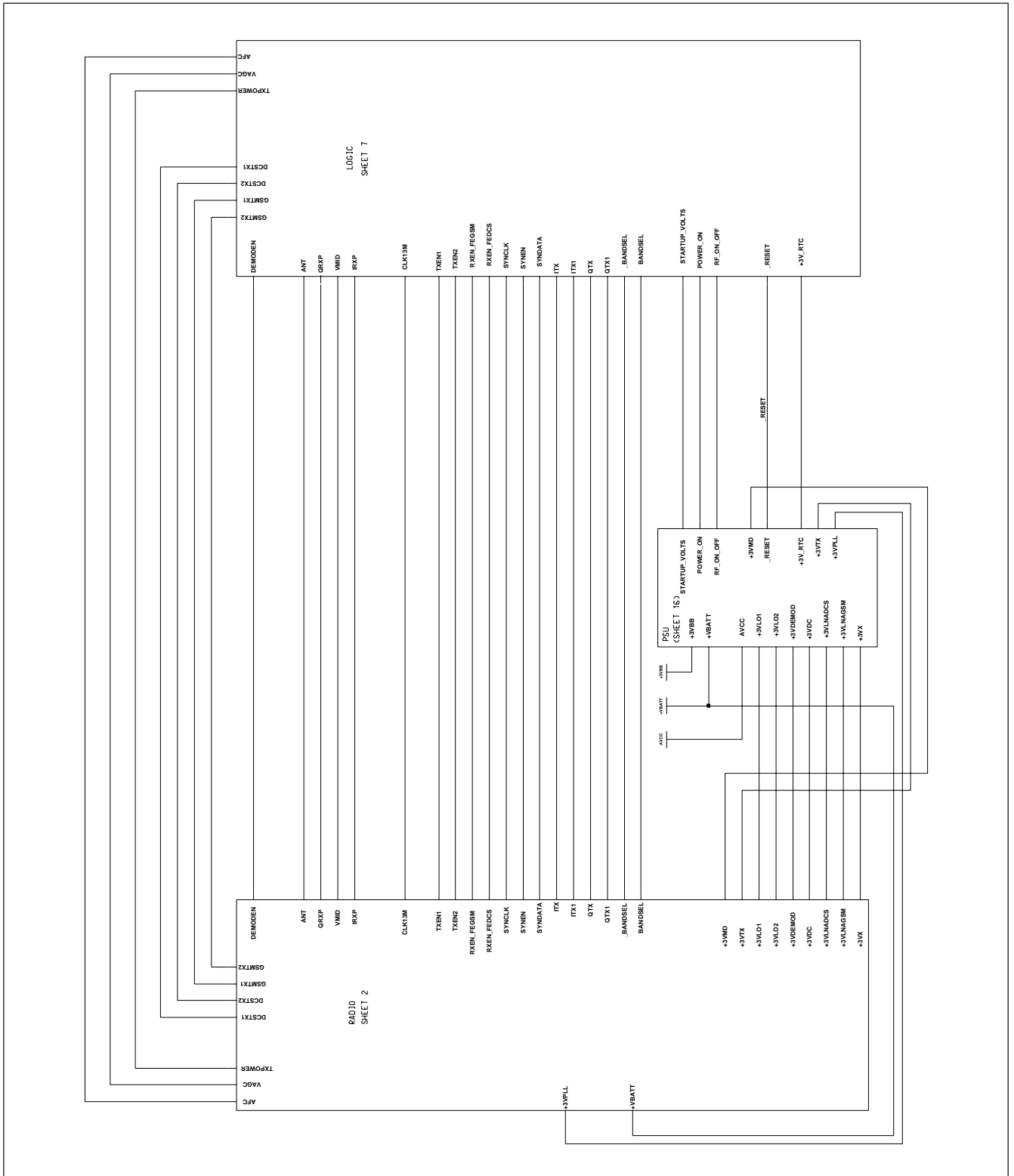
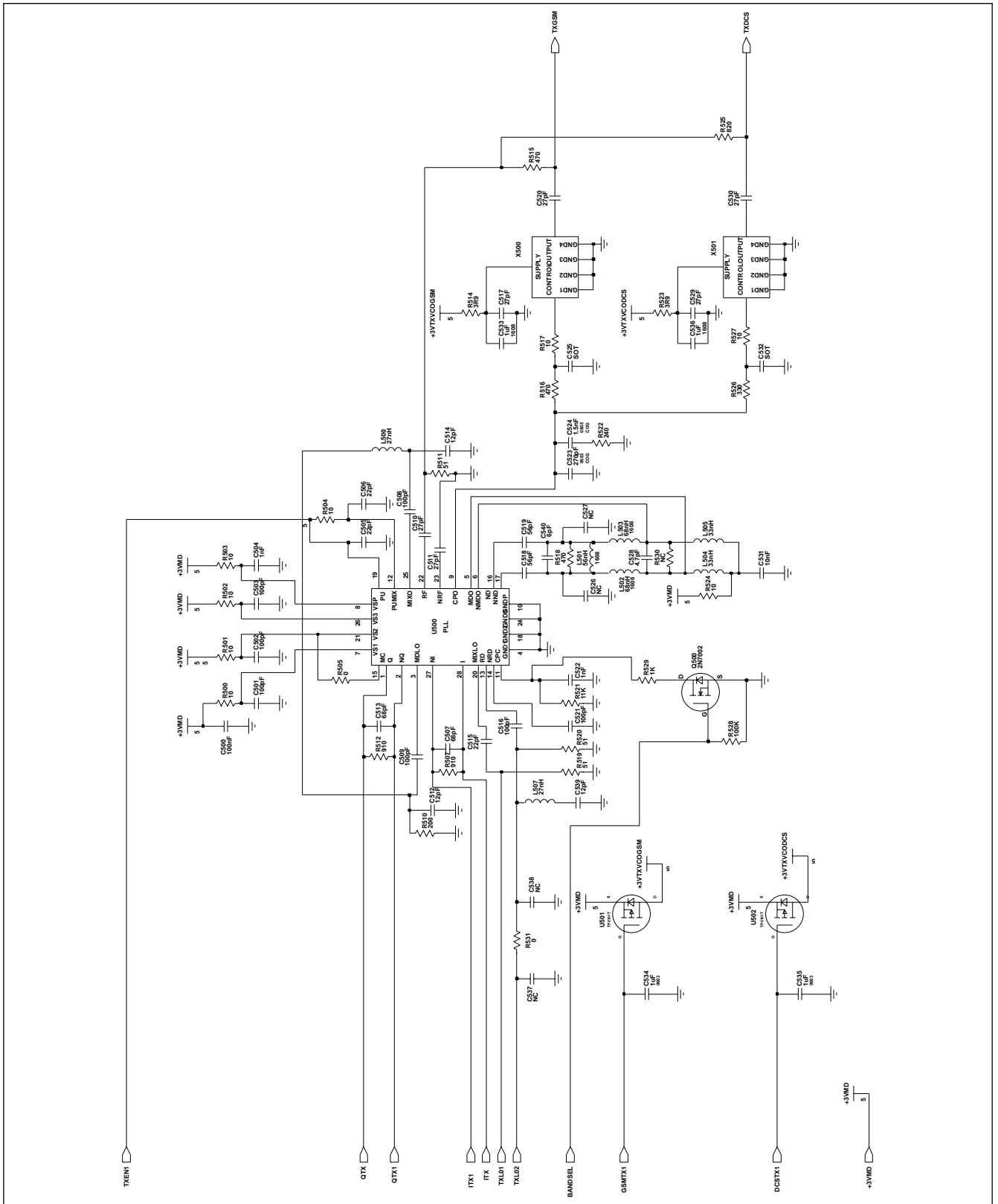


5. Schematic Diagrams

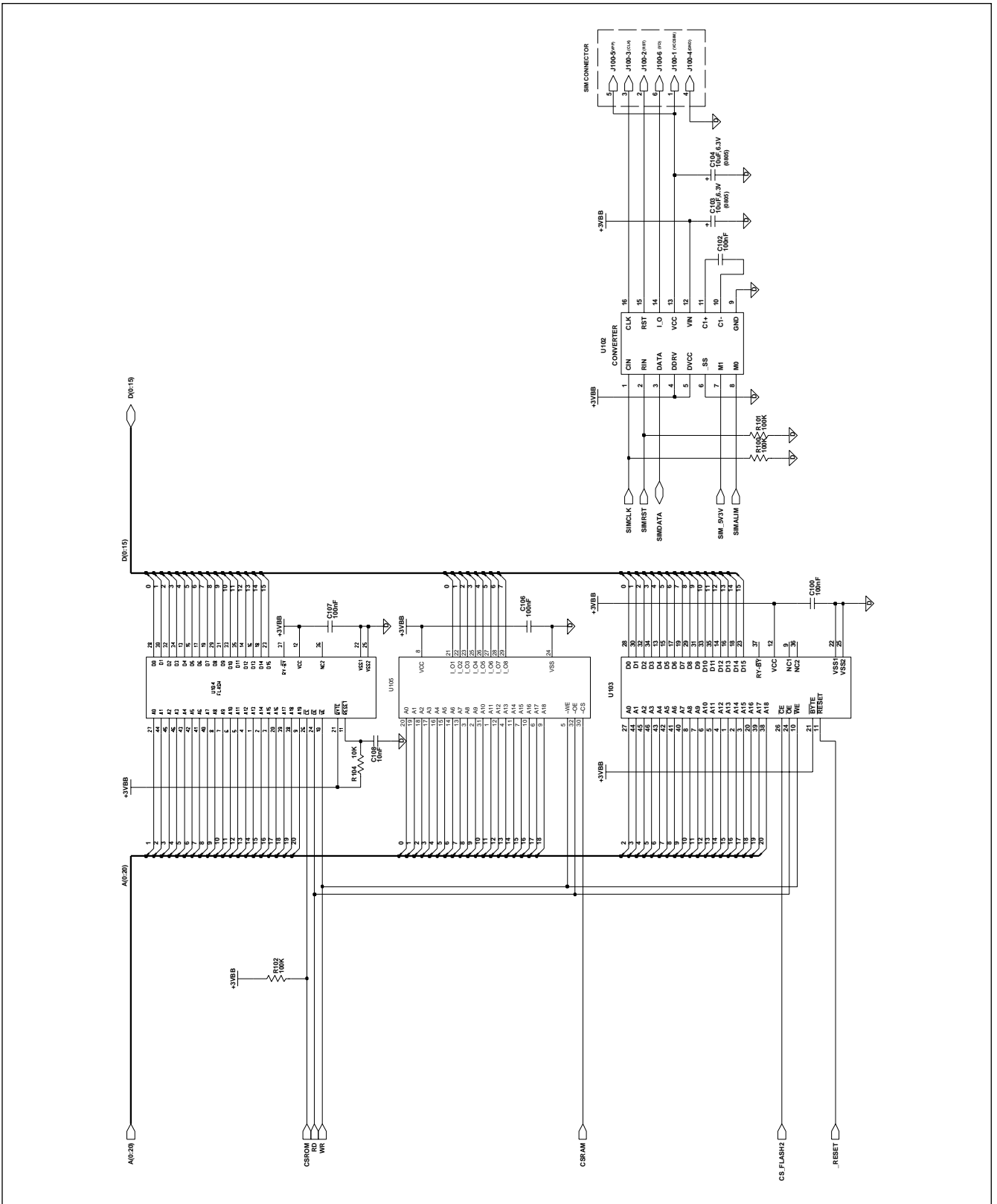
5-1 Top Level



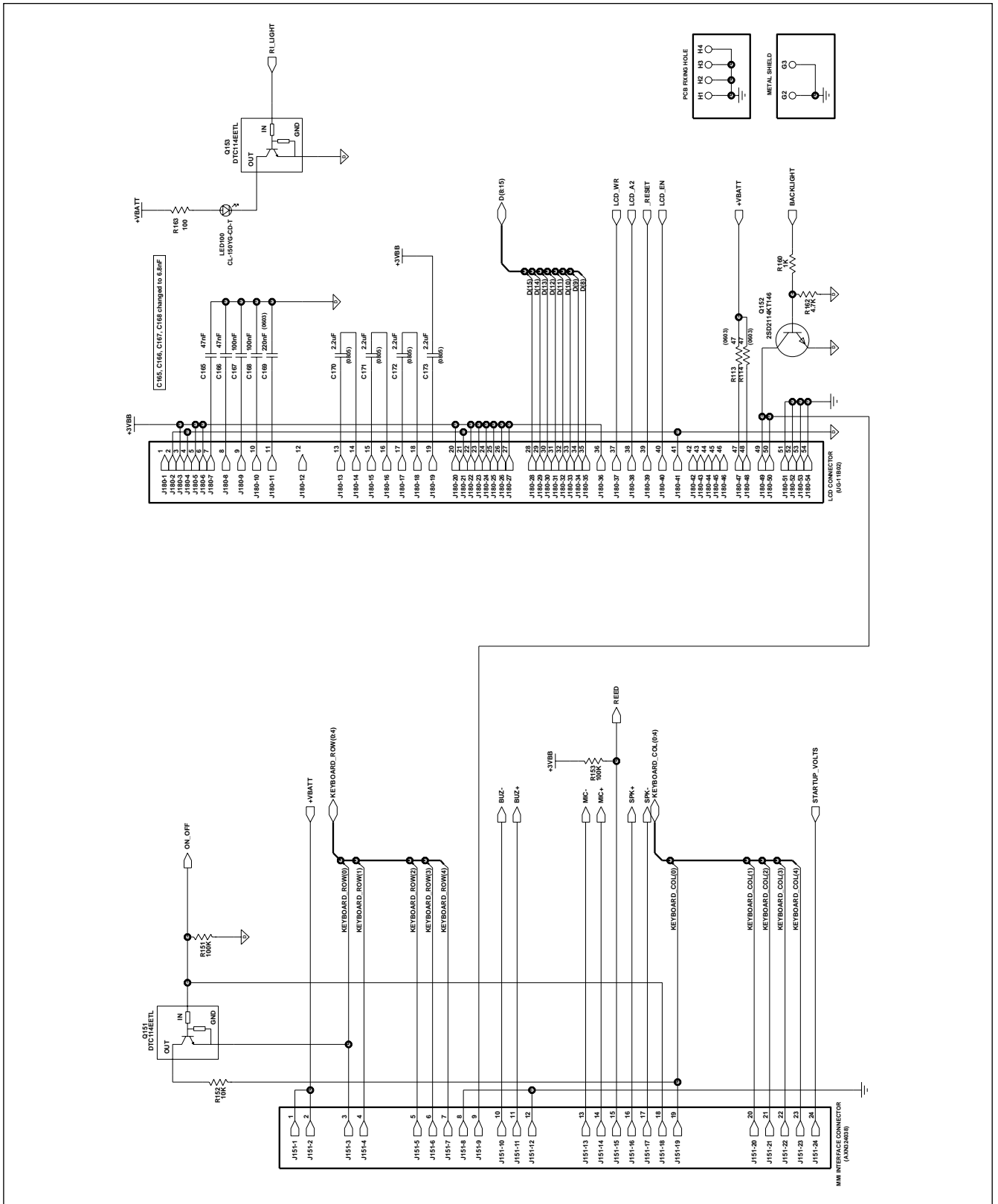
5-5 Modulator & Tx Vco



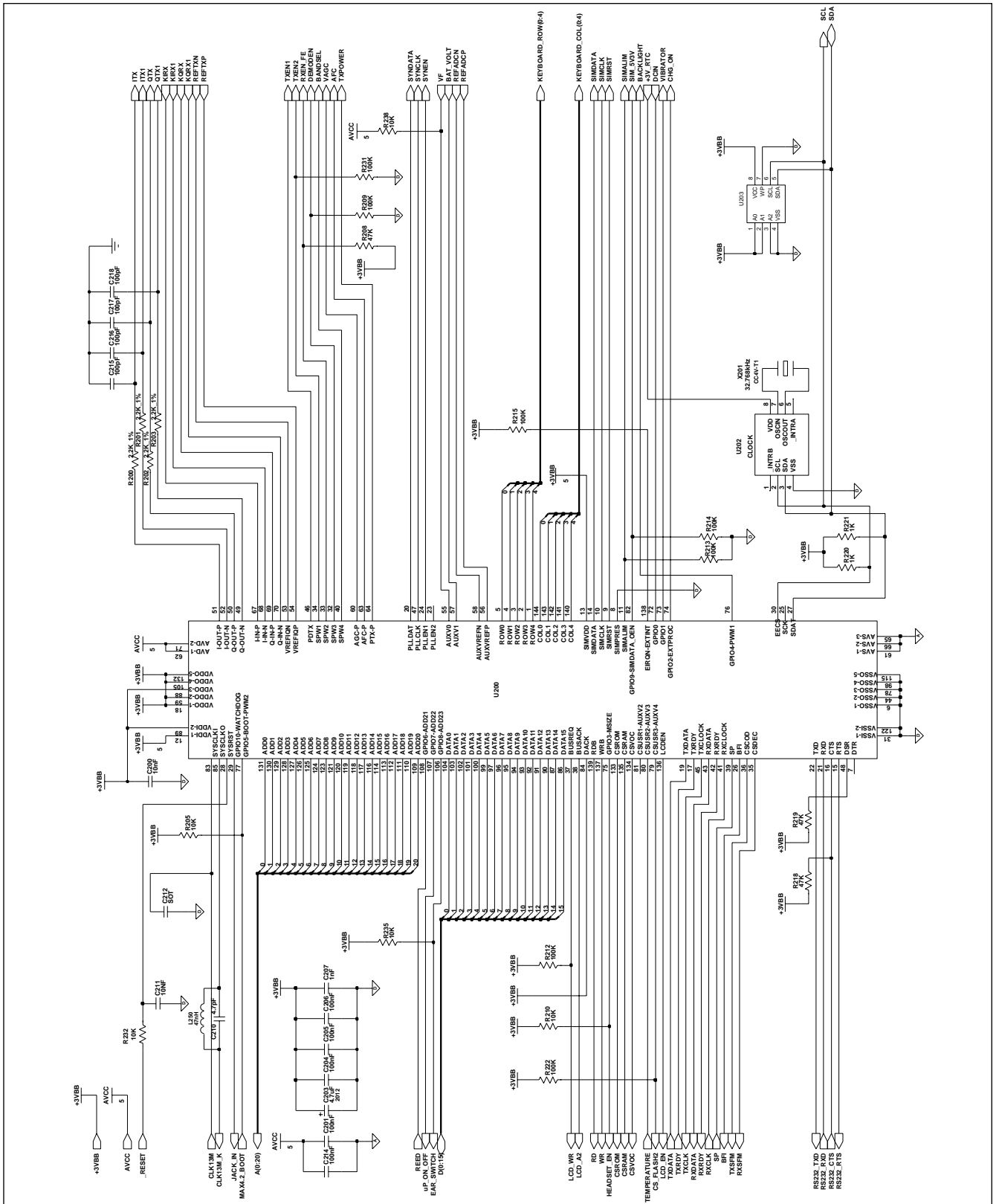
5-8 Memory & SIM Schematic



5-11 MMI Interface



5-12 Kernel



5-14 Control Logics

